



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 09/916,555  
Applicant : Chih Hsin Wang et al.  
Filed : July 26, 2001  
TC/A.U. : 2814  
Examiner : M. D. Pizarro-Crespo  
Title : A SEMICONDUCTOR MEMORY ARRAY OF FLOATING  
GATE MEMORY CELLS WITH LOW RESISTANCE SOURCE  
REGIONS AND HIGH SOURCE COUPLING  
(as amended)

Docket No. : 2102397-911400  
Customer No. : 26379

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 22, 2003.

GRAY CARY WARE & FREIDENRICH Date: 08/22/03

By: Kathleen LaBrie

Kathleen LaBrie

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION OF**

Sir:

In response to the Office Action of June 26, 2003, please amend the above identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 10 of this paper.

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